

**A Survey Of Analog-to-Digital Converter Technology
For Radar Applications By Robert C. Hicks**

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Efficient memory partitioning for parallel data

Many designs have been proposed and realized with traditional CMOS technology or analog reconfigurable Digital Signal Processing (DSP) applications such

Patent us20050017488 - weight measuring systems

which receives and displays the reflected light and an analog to digital converter Technology with Analog Technology for Automotive Applications

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Abstract. The paper begins with an overview of the many application fields of analog-to-digital converter modeling techniques, then makes a general classification of

Patent us8522303 - method for the distribution of

CROSS-REFERENCES TO RELATED APPLICATIONS. This data flow in WAV format is then applied to the input of a digital-analog converter 132 to Industrial Technology

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broadcast applications. Digital radar systems commonly use analog-to-digital converters to "Analog-to-digital converter survey and

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An inexpensive, buffered analog-to-digital converter for use on the SCSI bus with the UNIX operating system (Open-file report / United States Department of Interior

A survey of analog-to-digital converter technology

Abstract The first section of this report discusses sample and hold devices (SHD) which are required prior to the analog to digital converter (ADC) in most radar

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